



## Pin Name Abbreviations

### Summary

Library Standard  
LS0001 (v1.2) March 19, 2004

In certain instances, it may be desirable to abbreviate the pin names of schematic symbols. The following list contains commonly used abbreviations found in the Altium libraries.

Due to specific restraints and considerations, these mnemonics are not applied universally to every library component.

### Explanatory Notes

\* Abbreviation from appendix A of ANSI/IEEE Std. 991 - 1986 (Current Nov 1996).

\*\* Abbreviation conjoined as a prefix or postfix to an existing pin name.

### Pin Abbreviations

Name	Abbrev.	**	Name	Abbrev.	**
Access	ACC		Antenna	ANT	
Acknowledge *	ACK		Application	APPL	
Acquisition	ACQ		Arbitration	ARB	
Activate *	ACT		Asynchronous *	ASYNC	
Activation	ACT		Asynchronous Preset-Enable	APE	
Adder	ADD		Asynchronous/Synchronous	A/S	
Address *	ADR	A#	Audio	AUD	
Address Latch Enable	ALE		Auto Reset	AR	
Address Strobe	AS		Auto Zero	AZ	
Adjust	ADJ		Auxiliary	AUX	
Adjustment	ADJ				
Alarm	ALM		Back	BCK	
Alarm Inhibit *	ALI		Balance	BAL	
Alternate	ALT		Band Pass	BP	
Amplifier	AMP		Band Pass Filter	BPF	
Analog	ANLG	A	Bandwidth	BW	
Analog Ground	AGND		Basic	BSC	
Analog Supply	AVCC		Bass	.	B
Analog Vcc	AVCC		Battery	BAT	
Analog Vee	AVEE		Bias Voltage	VB	
Angle	ANG		Bin/Dec	B/D	
Anode	A		Binary *	BIN	

## Schematic Components – Pin Name Abbreviations

Binary/Decade	B/D		Cathode	K	
Bipolar Offset	BIPO		Channel	CH	
Bit *	BIT	B#	Channel Guard	CG	
Bit Counter *	BCTR		Character	CHAR	
Blank	BNK		Charge	CHG	
Blanker	BNK		Charge Pump	CP	
Blanking	BNK		Check *	CHK	
Blanking Input	BI		Chip Address	CA	
Blend	BLE		Chip Enable *	CE	
Block *	BLK		Chip Select *	CS	
Blue	.	B	Chrominance	CHROMA	
Booster	BST		Circuit	CCT	
Bootstrap	BOOT		Clamp	CLM	
Borrow	BRW		Clamping	CLM	
Borrow Output *	BO		Clear *	CLR	
Brake	BRK		Clear To Send	CTS	
Breakpoint	BKPT		Clock *	CLK	CK
Bridge	BRG		Clock Down	CPD	
Brightness	BRT		Clock Enable	CKEN	
Buffer *	BUF		Clock In	CLKI	
Buffered *	BUF		Clock Out	CLKO	
Burst	BUR		Clock Up	CPU	
Buzzer	BZ		Clockwise	CW	
Bypass	BYP		Coefficient	COEF	
Byte *	BYT		Coincidence	COIN	
Byte Enable	BE		Collector	COLL	
			Collision	CLSN	
Calibration	CAL		Collision Detected	CDT	
Cancel	CAN		Column *	COL	C
Capacitor	CAP		Command *	CMD	
	Cext		Common	COM	
	Cint		Commutate	CMUT	
	Cn		Comparator	CMPR	
	Cn+1		Compare *	COMP	
Carrier Detect	CRD		Compensation	COMPEN	
Carrier Sensed	CRS		Compensation Capacitor	CC	
Carrier Tuning	CTUN		Complement	COMPL	
Carry *	CRY		Composite	CMPOS	
Carry Input *	CI		Compressor	CMPRS	
Carry Output *	CO		Configuration	CFGN	
Cascade	CASC		Constant	CONST	
Cassette	CASS		Content-Addressable Memory	CAM	

## Schematic Components – Pin Name Abbreviations

Contrast	CNAST		Deemphasis	DEEM	
Control *	CNTL		Define	DEF	
Control Input	CTLI		Delay *	DLY	
Control Output	CTLO		Delete	DEL	
Conversion	CVS		Demagnetization	DEMAG	
Convert	CONV		Demodulator	DMD	
Converters	CONV		Demultiplexer	DX or DMUX	
Coprocessor	COPROC		Designation	DES	
Corrected *	CORR		Detect	DET	
Correction	CORR		Detector	DET	
Count *	CNT		Device *	DEV	
Counter *	CTR		Diagnostic	DIAGN	
Counter Clockwise	CCW		Diameter	DIA	
Crystal	XTAL		Differential	DIFF	
Crystal In	XTI		Dig Gnd	DGND	
Crystal Out	XTO		Digital	DIG	D
Current	CURR	I	Digital Ground	DGND	
Current Boost	IBST		Digital Supply	DVCC	
Current Limit	ILIM		Digital Vee	DVEE	
Current Reference	IREF		Diode	.	D
Current Sense	ISEN		Direct	DIR	
Current Sensing	ISEN		Direct-Current	DC	
Cycle *	CYC		Direction	DIR	
			Disable *	DIS	
Darlington Drive	DD		Discharge	DISC	
Data *	D	D#	Disconnect	DISCT	
Data Available	DAV		Display	DPY	
Data Clock	DCLK		Display Frequency	DF	
Data Input	DI		Divide	DIV	
Data Memory	DM		Divider	DIV	
Data Output	DO		Down *	DWN	
Data Ready	DR		Down/Up	U/D	
Data Set Ready	DSR		Drive	DRV	
Data Strobe	DAS		Driver *	DRV	
Data Terminal Ready	DTR		Driving	DRV	
Data Transfer Acknowledge	DTACK		Dummy	DUM	
Data Valid	DV		Duration	DUR	
Deadtime	DT		Dynamic	DYN	
Decimal *	DEC				
Decoder	DECO		Earphone	EAR	
Decouple	DCPL		Emitter	EMIT	EM
Decoupling	DCPL		Emphasis	EMP	

## Schematic Components – Pin Name Abbreviations

Enable *	EN		Gain Control	GC	
Enable Output	EO		Gain Reference	GREF	
Encode(r)	ENC		Gate	GT	
End Of Conversion	EOC		Generate *	GEN	
Enquiry	ENQ		Generator	GEN	
Erase *	ERS		Global	.	G
Error *	ERR		Global Write *	GW	
Excitation	EXC		Good	GD	
Expand	EXP		Green	.	G
Expander	EXP		Ground *	GND	
Expansion	EXP		Guard	GUD	
External *	EXT		Guard Hi-Z	ZGUD	
External Clock	EC				
External Crystal Oscillator	EXTAL		Handshake	HSHK	
			Heat Sink	HSNK	
Fail Safe	FLS		Height	HGT	
Fast/Trickle	F/T		High	.	HI
Fault *	FLT		High Pass	HP	
Fault Ground	FGND		High Pass Filter	HPF	
Feed Forward	FF		Hold	HLD	
Feedback	FB		Holding *	HLD	
Feedthrough	FT		Hook	HK	
Field *	FLD		Horizontal *	HORZ	H
Figure	FIG		Hysteresis	HYS	
Filter	FILT				
Fine Adjust	FADJ		Identification *	ID	
Fine Tuning	FTUN		Identity	ID	
First-In First-Out Memory	FIFO		Ignition	IGN	
Flyback	FLY		Increment	INCR	
Force	FRC		Index	.	X
Forced	FCD		Indicator	IND	
Format	FMT		Information	INFO	
Frame	FRM		Inhibit *	INH	
Frame Sync	FS		Initialization	INIT	
Frame Synchronization	FS		Input *	IN	
Frequency	FREQ	F	Input/Output *	IO	
Frequency Clamp	FC		Interface *	INTFC	
Frequency Compensation	FCOMP		Intergrator	INTG	
Frequency Reference	FREF		Intermediate Frequency	IF	
Function *	FNC		Internal *	INT	
			Interrupt *	INTRPT	
Gain	GN	G	Interrupt Acknowledge	IACK	

## Schematic Components – Pin Name Abbreviations

Interrupt Request *	IRQ		Low Pass	LP	
Invert	INV		Low Pass Filter	LPF	
Inverter	INVR		Low Voltage	LV	
Inverting Input	IN-		Low Voltage Comparator Out	LVCO	
Isolated	ISO		Lower Byte	LB	
Jam	.	J#	Magnitude	MAG	
Junction	JN		Mask *	MSK	
			Master *	MSTR	
Kelvin	K		Master Reset	MR	
Keyboard *	KYBD		Max/Min	MA/MI	
Keystone	KEY		Maximum	MAX	
Kill/Killer	KL		Memory *	MEM	
			Memory Enable	ME	
Lamp	LMP		Microcontroller Power Supply	VMC	
Lamp Test	LT		Microphone	MIC	
Latch *	LCH		Middle	MID	
Latch Enable	LE		Minimum	MIN	
Latched *	LCH		Mixer	MIX	MX
Leak	LK		Mode	MD	M#
Leakage	LKG		Mode Select	MS	
Least Significant Bit *	LSB		Model	MDL	
Least Significant Byte *	LSBYT		Modulator	MOD	
Left *	LFT	L	Monitor	MON	
Level	LEV		Monitoring	MON	
Library	LIB		Most Significant Bit *	MSB	
Limit	LIM		Most Significant Byte *	MSBYT	
Limitation	LIM		Motor *	MTR	
Limiter	LIM		Multiplex *	MPX	
Line Fault	LF		Multiplexed	MPX	
Line Signal Detect	LSD		Multiplexer	MPX	
Linearity	LIN		Multiplexer/Demultiplier	MDX	
Load *	LD		Multiplier *	MULT	
Local	.		Multiply *	MULT	
Local Oscillator	LO		Mute	MUT	
Location *	LOC				
Lock	LCK		Negative *	NEG	
Lock Detector	LDE		Negative Acknowledge	NACK	
Locked	LCK		Network	NTWK	
Loudspeaker	LS		No Connect	NC	
Low	.	LO	Noise Reduction	NR	
Low Line	LL		Non-Inverting Input	IN+	

## Schematic Components – Pin Name Abbreviations

Normal	NORM		Playback	PB	
Not Connected	NC		Point	PT	
Not To Be Connected	DNU		Polarity	POL	
Number	NUM		Position	POS	
			Power *	PWR	P
Octal	OCT		Power Fail Input	PFI	
Offset	OS		Power Fail Output	PFO	
Off-Time	TOFF		Power Ground	PGND	
Op Amp	OPA		Power On Reset	POR	
Operating	OP		Power Output	POUT	
Organization	ORG		Power Up	PRUP	
Oscillator	OSC		Powerdown	PDN	
Oscillator In	OSCI		Predistortion Control	PDC	
Oscillator Out	OSCO		Preset	PRE	
Output *	OUT	O	Preset Enable	PSE	
Output Control	OC		Processor *	PROC	
Output Enable	OE		Program *	PRGM	
Over Flow *	OVF		Programmable Read-Only Memory	PROM	
Over Range	OVR		Protection	PROT	
Overhead	OVH		Pull	PUL	
Overload	OL		Pump Capacitor	CP	
Overvoltage	OV		Push	PSH	
Overvoltage Protection	OVP				
			Quadrature	QUAD	
Parabola	PARO		Quiescent	QUIESC	
Parallel	P		Quotient	QUOT	
Parallel Enable	.				
Parallel Load	PLD		Radio	RAD	
Parallel/Serial	P/S		Ramp	RMP	
Parameter	PARAM		Random-Access Memory	RAM	
Parity *	PAR		Range	RNG	
Parity Error *	PE		Read *	RD	
Pattern	PAT		Read/Write	R/W	
Peak	PK		Read-Only Memory	ROM	
Phase	PH		Ready *	RDY	
Phase Detector	.		Ready/Busy	RY/BY	
Phone	PHON		Receive	RX	R
Physical	PHY		Receive Clock	RXC	
Picture	PIC		Receive Data	RXD	
Pilot	PIL		Recirculation	RECIRC	
Pipeline	PL		Record	REC	
Pixel	PX		Recording	REC	

## Schematic Components – Pin Name Abbreviations

Recovered Audio	RA		Sandcastle	SAND	
Rectification	RECT		Saturation	SAT	
Rectifier	RECT		Sawtooth	SAWTH	
Red	.	R	Search	SRCH	
Reference	REF		Segment	SEG	
Reference Current	IREF		Select *	SEL	
Reference Voltage	VREF		Select Enable	SE	
Refresh *	RFSH		Sense	SEN	
Register Select	RS		Sensitivity	SEN	
Register/Counter	R/C		Sensor	SENR	
Regulated	REG		Separator	SEP	
Regulation	REG		Sequence	SEQ	
Regulator	REG		Sequencer	SEQ	
Regulator Voltage	VREG		Serial	SER	S
Reject *	REJ		Serial Clock	SCK	
Remote	REM		Serial Data	SDA	
Report	RPT		Serial Data In	SDI	
Request *	REQ		Serial Data Out	SDO	
Request To Send	RTS		Serial Input	SI	
Reserved	RESV		Serial Mode Select	SMS	
Reset	RST	R	Serial Output	SO	
Resistor	RES	R#	Service Request	SRQ	
.	Rext		Servo	SRV	
.	Rint		Servo Amplifier	SRVA	
Resolution	RESOL		Set	.	S
Response Time	RSPT		Shape Factor	SF	
Retain	RTAN		Shield	SHLD	
Return *	RTN		Shift *	SFT	
Reversal	RVS		Shift Left	SL	
Reverse	RVS		Shift Register	SRG	
Right	.	R	Shift Right	SR	
Ringing	RING		Shift/Load	S/L	
Ripple Counter	RCTR		Short Circuit	SC	
Ripple-Blanking Input	RBI		Shut Down	SD	
Ripple-Blanking Output	RBO		Shutdown	SD	
Rise	RIS		Sign Extend	SNEX	
Roll-Off	RO		Signal	SIG	
Row *	ROW	R#	Signal Ground	SGND	
			Silence	SLN	
Σ	SUM	S	Simulation *	SIM	
Sample	SMPL		Skew	SKW	
Sample/Hold	S/H		Slave *	SLV	

## Schematic Components – Pin Name Abbreviations

Sleep	SLE	Tachometer	TACH
Slew Rate	SLWR	Temperature	TEMP
Slope	SLP	Terminal *	TERM
Soft Start	SS	Terminate *	TERM
Soft-Start	SS	Termination	TERM
Sound	SND	Terminations	TERM
Source	SRC	Test *	TST
Speaker	SPK	Test Point	TP
Spindle	SPN	Thermal	THERM
Square	SQ	Thermal Shutdown	TS
Square Wave	SQW	Threshold	THR
Squelch	SQEL	Time Constant	TC
Stabilized	STAB	Timing	TIM
Stack *	STK	Timing Capacitor	CT
Stage	STG	Timing Resistor	RT
Standard	STD	Toggle *	TG
Standby	SBY	Tolerance	TOL
Start	STA	Torque	TRQ
Status	STAT	Transceiver *	XCVR
Stereo	ST	Transient	TRAN
Streaming	STR	Transmission	TRANS
Stretch	STRCH	Transmit	TX
Strobe	STB	Transmit Clock	TXC
Substrate	SUB	Transmit Data	TXD
Summing	SUM	Transmit Sync	TSYNC
Summing Junction	SJ	Transmitter Enable	TEN
Supply	VS	Transparent	TRN
Supply Voltage	VS	Treble	TR
Sustaining	SUS	Triangle	TRI
Switch *	SW	Trigger *	TRIG
Switched	SW	Triggering	TRIG
Switching	SW	True Complement	T/C
Syllabic	SYL	Tuner	TUN
Synchro	SYNC	Tunning	TUN
Synchronization *	SYNC	Twisted Pair	TWP
Synchronize	SYNC	Typical	TYP
Synchronous	SYNC		
Synchronous Preset-Enable	SPE	Undervoltage	UV
Synthesizer	SYN	Up/Down	U/D
System *	SYS	Upper Byte	UB
System Clock	SYSCLK	Utility *	UTIL



## Schematic Components – Pin Name Abbreviations

Valid *	VLD	Watch Dog	WDG	
Varactor	VARAC	Watch Dog Input	WDI	
Vari Cap	VCAP	Watch Dog Output	WDO	
Variable	VAR	Waveshape	WVSH	
Vboot	BOOT	Word	WD	
Vertical *	VERT	V	Word Select	WS
Video *	VID	Write *	WR	
Virtual *	VIRT	Write Control	WC	
Voltage	VOLT	Write Enable	WE	
Voltage Regulator	VREG			
Volume	VOL	Zener	ZEN	
		Zero Adjust	ZADJ	
		Zero Current	ZC	
		Zero-Detect	ZD	

## Revision History

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Date	Version No.	Revision
16-May-2000	1.1	Addition and removal of abbreviations.
19-Mar-2004	1.2	Reformatted document.

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